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 11046 U.S. 846205
 09/02/01
 05/05/01

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P66632US0 GROUP ART UNIT: Not Yet Assigned
 SERIAL NO.: Not Yet Assigned FILING DATE: May 2, 2001
 APPLICANT(S): Hoon LEE, et al. TODAY'S DATE: May 2, 2001

U. S. PATENT DOCUMENTS

| *EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | INT'L CLASS | SUB- CLASS | FILING DATE (If Appropriate) |
|----------------------|---------------------|-----------------|-----------------------|----------------|---------------|---------------------------------|
| <u>JP</u> | AA <u>6,144,712</u> | <u>11/07/00</u> | <u>Samueli et al.</u> | <u>375</u> | <u>371</u> | <u>10/09/97</u> |
| | AB _____ | _____ | _____ | _____ | _____ | _____ |
| | AC _____ | _____ | _____ | _____ | _____ | _____ |

FOREIGN PATENT DOCUMENTS

| DOCUMENT NUMBER | DATE | COUNTRY | SUB- CLASS | TRANSLATION (YES) (NO) |
|--------------------|-------|---------|---------------|---------------------------|
| AD _____ | _____ | _____ | _____ | _____ |
| AE _____ | _____ | _____ | _____ | _____ |

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

JP AF Samueli et al.; VLSI Architectures for a High-Speed tunable Digital Modulator/Demodulator/Bandpass-Filter Chip Set; Integrated Circuits and Systems Laboratory Electrical Engineering Department, University of California, Los Angeles; 1992 IEEE, Pages 1065-1068.

JP AG D'Luna et al.; A Single-Chip Universal Code Set-Top Box/Modem Transceiver; IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999, Pages 1647-1660.

EXAMINER JMdh DATE CONSIDERED 1/6/04

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).